y S. Mailloux et al.

SYNCHRONOUSLY-ACCESSIBLE MEMORY DEVICE WITH MODE SELECTION CIRCUITRY FOR BURST OR PIPELINED OPERATION 2700

Docket No.:

303.623US1

Filed: Examiner: May 20, 1996

Customer No.: 21186

Hong Kim

Due Date: January 13, 2004

Group Art Unit: 2186 Confirmation No.: 2941

Serial No.: 08/650719

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